

Job Description

Title: Senior _Senior FPGA Engineer _____ Cost Ctr: _____
Work Location: _Addison, TX_____ Position Status: Full-time Part-time
Division & Department: Engineering
Position Reports to: Manager, Banknote Systems

JOB SUMMARY

The Senior FPGA Engineer will be responsible for designing, simulating and verifying FPGAs in a multi-physics solutions environment with an emphasis on high speed digital signal processing hardware.

SUMMARY OF ESSENTIAL JOB FUNCTIONS

The essential functions listed below are representative of the functions that must be performed in order to satisfactorily fulfill the purpose of this job. Additional functions and duties may be assumed or assigned from time to time.

- Write VHDL code, perform design entry, simulation, synthesis and debug of integrated functional blocks that support high speed inter-processor communications
- Implement high speed data transfer mechanisms (10Gbit/sec) between processing engines and/or memory via serial interfaces (SATA, PCIE, SRIO, etc.)
- Utilize IDE and JTAG based hardware debug and programming tools along with logic analyzers and oscilloscopes to verify hardware performance and commission new hardware revisions
- Conduct and participate in design reviews
- Must be well organized and be able to work with minimal supervision
- Must be able to manage internal and external resources
- Excellent written and verbal communication skills
- Excellent technical problem solving

KNOWLEDGE, SKILLS AND ABILITIES

To perform this job successfully, an individual must be able to perform each essential function satisfactorily. The requirements listed below are representative of the knowledge, skills and/or abilities required to satisfactorily perform the essential functions of this job.

- BS in Engineering from an ABET accredited college or equivalent
- 10 years of relevant high-speed digital logic experience
- Hands-on experience with Altera/ModelSim design flow is strongly preferred
- Hands-on experience with SATA, PCIE, or SRIO IP is strongly preferred
- Hands-on experience with DDR2 SDRAM IP is strongly preferred

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WORK REQUIREMENTS

The work environment characteristics are representative of those an employee encounters while performing the job. Authentix is committed to complying with all applicable provisions of the Americans with Disabilities Act (ADA), the ADA Amendments of 2008 and all applicable state and local laws concerning disability accommodation. Reasonable accommodations will be provided to individuals with known physical or mental disabilities if such accommodation would not impose an undue hardship on the company, and would enable the individual to apply for, or perform, the essential functions of the position in question.

Environment: Work is performed primarily in a standard office environment. Employees may work under the stress of regular interdepartmental interaction and pressure to meet various deadlines.

Physical: Essential functions require sufficient physical ability and mobility to work in a *lab* setting. While performing the duties of this job, the employee is frequently required to stand and/or sit for prolonged periods of time; must be able to hear and verbally communicate in order to exchange information in person or over the phone; to occasionally stoop, bend, kneel, crouch, reach and twist; to lift, carry, push and/or pull up to 25 pounds of weight; to operate office equipment requiring repetitive hand movement; to occasionally travel to other locations using various modes of private or commercial transportation.

NOTIFICATION

This Job Description is intended to describe the general nature and level of work being performed by people assigned to this job and is not considered an exhaustive list of all responsibilities, duties and required skills. This Job Description does not constitute an offer of employment. The employment relationship between the Company and its employees is "At-Will" and based on mutual consent. Authentix, Inc. is an Equal Opportunity Employer.